



SVSA2800S Series

HIGH RELIABILITY HYBRID RADIATION TOLERANT DC-DC CONVERTERS

DESCRIPTION

The SVSA series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Paramount to the SVSA series is a magnetic feedback circuit that is radiation immune. Operating at a nominal fixed frequency of 450 kHz, these regulated, isolated units utilize well controlled undervoltage lockout circuitry to eliminate slow start-up problems. The SVSA series has been characterized and tested for TID (Total Ionizing Dose) at HDR (High Dose Rate) and LDR (Low Dose Rate – ELDRS) per VPT's RHA plan. The SVSA series has also been characterized for SEE (Single Event Effects). VPT's certified radiation program per MIL-PRF-38534, Appendix G is currently under review by DSCC. Please contact DSCC at 614-692-0585 for details. This characterization and testing is performed at the critical semiconductor component piece-part level (RLAT) from traceable semiconductor lots as well as on the SVSA series hybrid converter level produced from the same traceable semiconductor lots evaluated during RLAT.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 Class H and Class K and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433
 6,005,780 6,084,792 6,118,673

FEATURES

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 6 Watts Output Power
- Radiation Immune Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Projection Welded Hermetic Package
- High Power Density: > 19 W/in³
- Custom Modified Versions May Be Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVMA28 EMI Filter
- MIL-PRF-38534 Element Evaluated Components Utilized
- Characterized and assured to 30krads(Si), per VPT's RHA plan specified per MIL-PRF-38534, Appendix G, Level P with 2X margin. After radiation exposure, converter testing is performed at 25°C per standard datasheet limits.
- Characterized to 44 MeV-cm²/mg with minor transients only; no dropouts, shutdowns, latch up or burn out.
- Critical semiconductor component piece-parts and assured converter products tested at an HDR of 80 rads(Si)/sec and an LDR of 8 mrads(Si)/sec.



Figure 1 – SVSA2800S DC-DC Converter (Exact marking may differ from that shown)

SPECIFICATIONS ($T_{CASE} = -55^{\circ}C$ to $+125^{\circ}C$, $V_{IN} = +28V \pm 5\%$, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V_{DC}	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power ¹	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}C$)	2.7 Watts	Weight (Maximum)	15 Grams

Parameter	Conditions	SVSA283R3S			SVSA2805S			Units
		Min	Typ	Max	Min	Typ	Max	
STATIC								
INPUT Voltage ⁴	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	-	6	-	-	6	mA
	No Load	-	-	60	-	-	60	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	-	50	-	-	50	mA _{p-p}
Inhibit Pin Input ⁴		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage ⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off ⁴		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage	V_{OUT} $T_{CASE} = 25^{\circ}C$	3.267	3.30	3.333	4.95	5.00	5.05	V
	V_{OUT} $T_{CASE} = -55^{\circ}C$ to $+125^{\circ}C$	3.25	3.30	3.35	4.925	5.00	5.075	V
Power ³		0	-	4	0	-	5	W
Current ³	I_{OUT}	0	-	1.21	0	-	1.0	A
Ripple Voltage	V_{OUT} Full Load, 20Hz to 10MHz	-	-	30	-	-	30	mV _{p-p}
Line Regulation	V_{OUT} $V_{IN} = 15V$ to $50V$	-	-	15	-	-	15	mV
Load Regulation	V_{OUT} No Load to Full Load	-	-	50	-	-	50	mV
EFFICIENCY		62	66	-	65	70	-	%
LOAD FAULT POWER DISSIPATION	Overload ⁴	-	-	3.3	-	-	3.3	W
	Short Circuit	-	-	3	-	-	3	W
CAPACITIVE LOAD ⁴		-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY		350	450	500	350	450	500	kHz
ISOLATION	$500 V_{DC}$	100	-	-	100	-	-	M Ω
MTBF (MIL-HDBK-217F)	SF @ $T_C = 55^{\circ}C$	-	830	-	-	830	-	kHrs
DYNAMIC								
Load Step Output Transient	V_{OUT} Half Load to Full Load	-	150	300	-	250	500	mV _{PK}
Load Step Recovery ²		-	450	700	-	450	700	μSec
Line Step Output Transient ⁴	V_{OUT} $V_{IN} = 16V$ to $40V$	-	250	500	-	350	700	mV _{PK}
Line Step Recovery ^{2, 4}		-	600	1200	-	600	1200	μSec
Turn On Delay	V_{OUT} $V_{IN} = 0V$ to $28V$	-	10	20	-	10	20	mSec
Turn On Overshoot		-	0	15	-	0	25	mV _{PK}

Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ($T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, $V_{IN} = +28\text{V} \pm 5\%$, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V_{DC}	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power ¹	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$)	2.7 Watts	Weight (Maximum)	15 Grams

Parameter	Conditions	SVSA2812S			SVSA2815S			Units
		Min	Typ	Max	Min	Typ	Max	
STATIC								
INPUT Voltage ⁴	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	-	6	-	-	6	mA
	No Load	-	-	60	-	-	60	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	-	50	-	-	50	mA _{p-p}
Inhibit Pin Input ⁴		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage ⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off ⁴		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage	V_{OUT} $T_{CASE} = 25^{\circ}\text{C}$	11.88	12.0	12.12	14.85	15.0	15.15	V
	V_{OUT} $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	11.82	12.0	12.18	14.775	15.0	15.225	V
Power ³		0	-	6	0	-	6	W
Current ³	I_{OUT}	0	-	0.5	0	-	0.4	A
Ripple Voltage	V_{OUT} Full Load, 20Hz to 10MHz	-	-	30	-	-	30	mV _{p-p}
Line Regulation	V_{OUT} $V_{IN} = 15\text{V}$ to 50V	-	-	15	-	-	15	mV
Load Regulation	V_{OUT} No Load to Full Load	-	-	50	-	-	50	mV
EFFICIENCY		71	75	-	72	76	-	%
LOAD FAULT POWER DISSIPATION	Overload ⁴	-	-	3	-	-	3	W
	Short Circuit	-	-	3	-	-	3	W
CAPACITIVE LOAD ⁴		-	-	500	-	-	500	μF
SWITCHING FREQUENCY		350	450	500	350	450	500	kHz
ISOLATION	500 V_{DC}	100	-	-	100	-	-	M Ω
MTBF (MIL-HDBK-217F)	SF @ $T_C = 55^{\circ}\text{C}$	-	830	-	-	830	-	kHrs
DYNAMIC								
Load Step Output Transient	V_{OUT} Half Load to Full Load	-	300	700	-	400	700	mV _{PK}
Load Step Recovery ²		-	200	400	-	200	400	μSec
Line Step Output Transient ⁴	V_{OUT} $V_{IN} = 16\text{V}$ to 40V	-	700	1200	-	700	1300	mV _{PK}
Line Step Recovery ^{2, 4}		-	200	600	-	200	600	μSec
Turn On Delay	V_{OUT} $V_{IN} = 0\text{V}$ to 28V	-	10	20	-	10	20	mSec
Turn On Overshoot		-	0	50	-	0	50	mV _{PK}

Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.
3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

SPECIFICATIONS ($T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, $V_{IN} = +28\text{V} \pm 5\%$, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V_{DC}	Junction Temperature Rise to Case	+10°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power ¹	6 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}\text{C}$)	2.7 Watts	Weight (Maximum)	15 Grams

Parameter	Conditions	SVSA285R2S			Units
		Min	Typ	Max	
STATIC					
INPUT Voltage ⁴	Continuous	15	28	50	V
	Transient, 1 sec	-	-	80	V
Current	Inhibited	-	-	6	mA
	No Load	-	-	60	mA
Ripple Current	Full Load, 20Hz to 10MHz	-	-	50	mA_{p-p}
Inhibit Pin Input ⁴		0	-	1.5	V
Inhibit Pin Open Circuit Voltage ⁴		9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	V
UVLO Turn Off ⁴		11.0	-	14.5	V
OUTPUT Voltage	V_{OUT} $T_{CASE} = 25^{\circ}\text{C}$	5.148	5.20	5.252	V
	V_{OUT} $T_{CASE} = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	5.122	5.20	5.278	V
Power ³		0	-	5.2	W
Current ³	I_{OUT}	0	-	1.0	A
Ripple Voltage	V_{OUT} Full Load, 20Hz to 10MHz	-	-	30	mV_{p-p}
Line Regulation	V_{OUT} $V_{IN} = 15\text{V}$ to 50V	-	-	15	mV
Load Regulation	V_{OUT} No Load to Full Load	-	-	50	mV
EFFICIENCY		65	70	-	%
LOAD FAULT POWER DISSIPATION	Overload ⁴	-	-	3.3	W
	Short Circuit	-	-	3	W
CAPACITIVE LOAD ⁴		-	-	1000	μF
SWITCHING FREQUENCY		350	450	500	kHz
ISOLATION	500 V_{DC}	100	-	-	$\text{M}\Omega$
MTBF (MIL-HDBK-217F)	SF @ $T_C = 55^{\circ}\text{C}$	-	830	-	kHrs
DYNAMIC					
Load Step Output Transient	V_{OUT} Half Load to Full Load	-	250	500	mV_{PK}
Load Step Recovery ²		-	450	700	μSec
Line Step Output Transient ⁴	V_{OUT} $V_{IN} = 16\text{V}$ to 40V	-	350	700	mV_{PK}
Line Step Recovery ^{2,4}		-	600	1200	μSec
Turn On Delay	V_{OUT} $V_{IN} = 0\text{V}$ to 28V	-	10	20	mSec
Turn On Overshoot		-	0	25	mV_{PK}

Notes: 1. Dependant on output voltage. 2. Time for output voltage to settle within 1% of its nominal value.
 3. Derate linearly to 0 at 135°C. 4. Verified by qualification testing.

BLOCK DIAGRAM

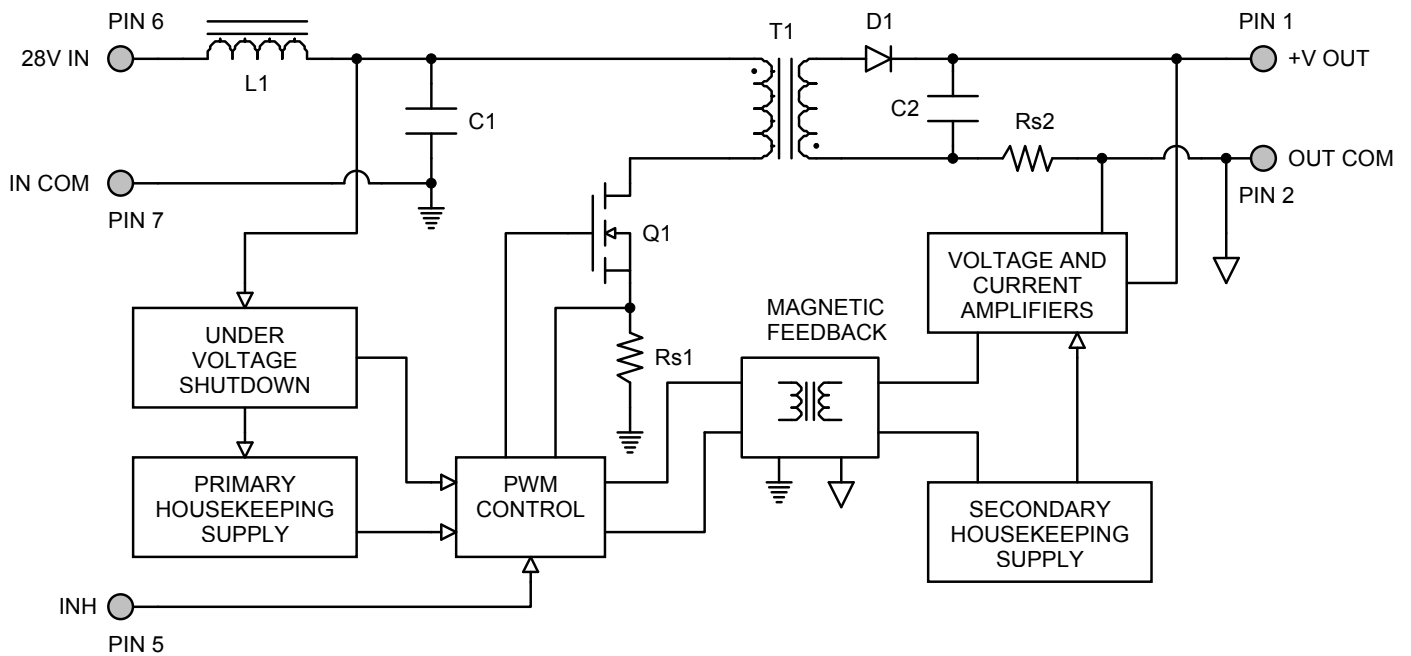


Figure 2

CONNECTION DIAGRAM

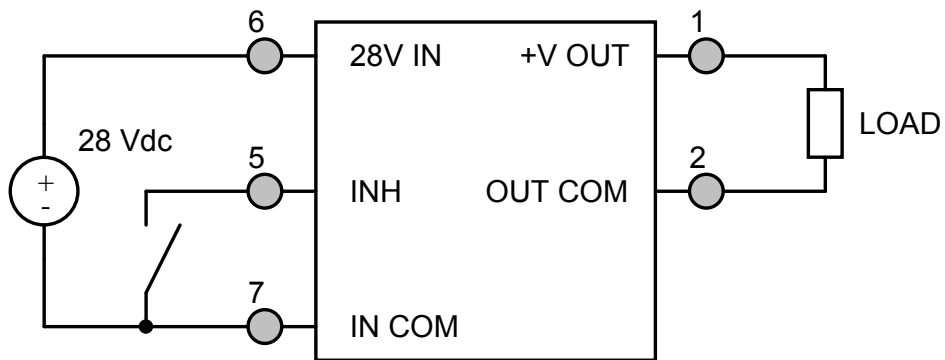


Figure 3

INHIBIT DRIVE CONNECTION DIAGRAMS

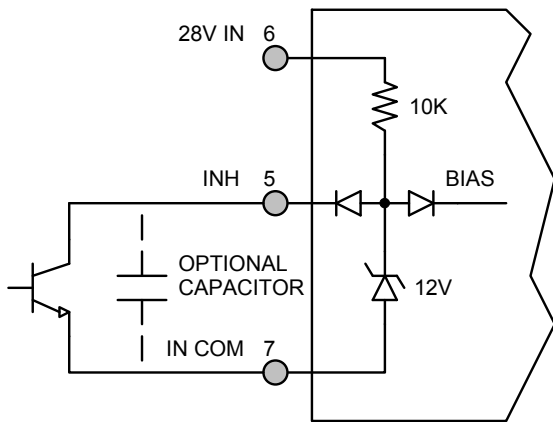


Figure 4 – Internal Inhibit Circuit and Recommended Drive
(Shown with optional capacitor for turn-on delay)

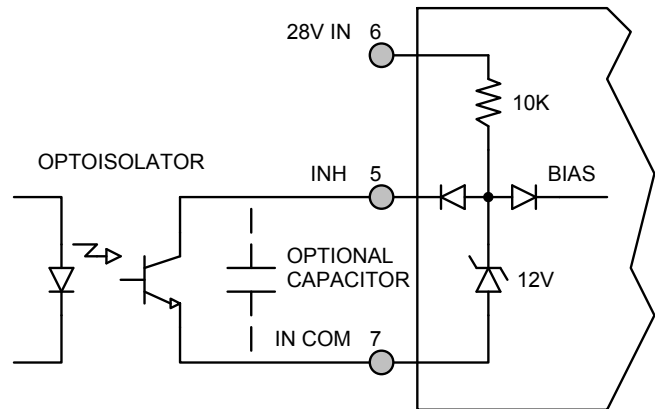


Figure 5 – Isolated Inhibit Drive
(Shown with optional capacitor for turn-on delay)

EMI FILTER HOOKUP DIAGRAM

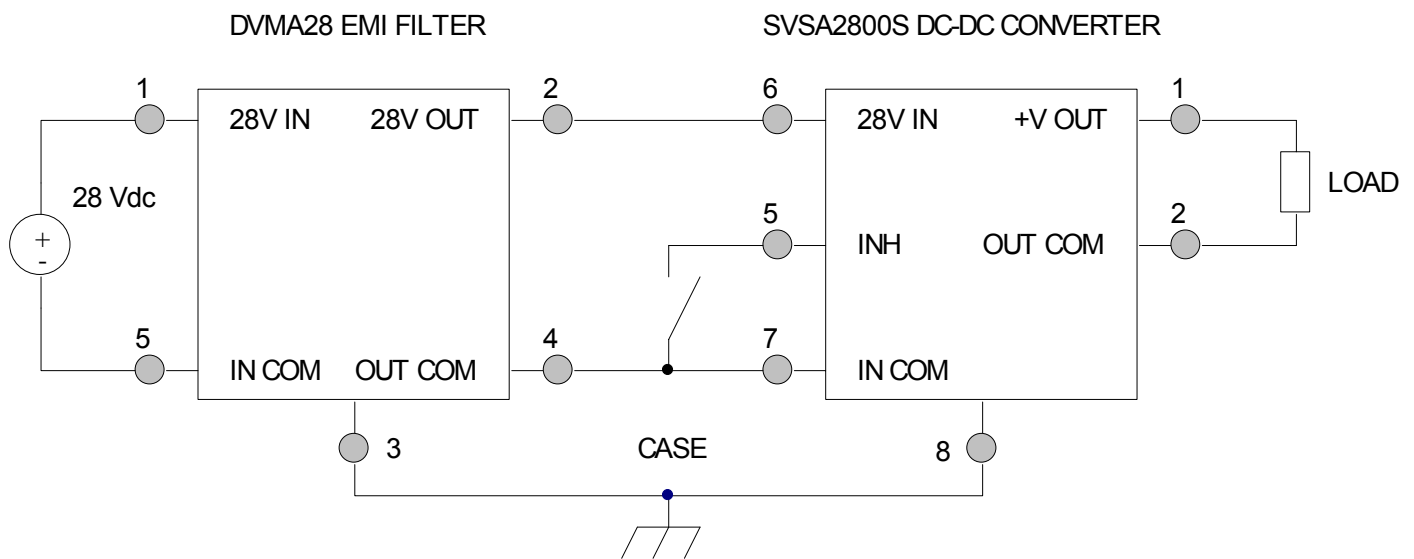


Figure 6 – Converter with EMI Filter

RHA TEST CIRCUIT DIAGRAM

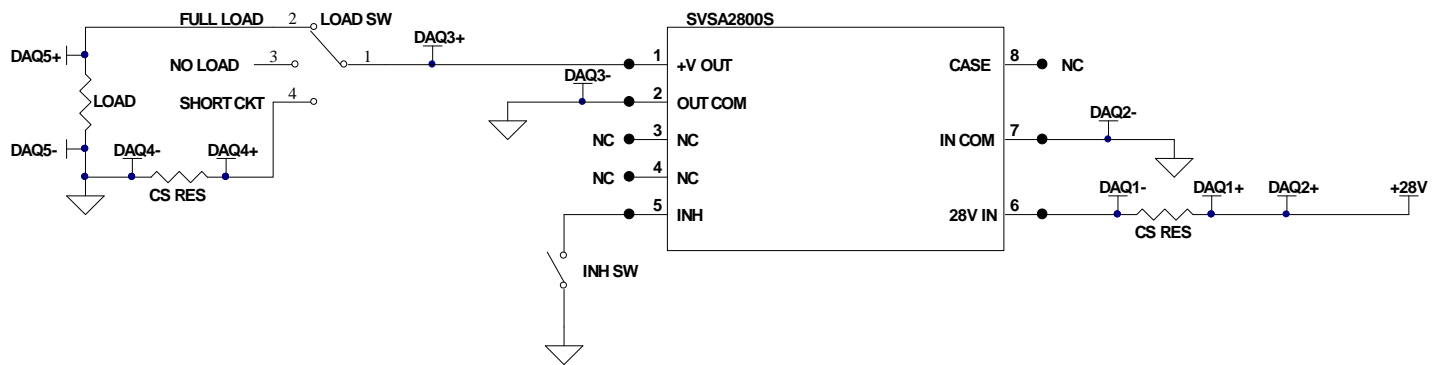


Figure 7

FREQUENCY OF RHA TESTING

Every initial wafer lot of critical semiconductor components has been characterized and tested at HDR as well as at LDR to determine if there is ELDRS sensitivity. If a specific component type is determined to have ELDRS sensitivity, all future wafer lots of that specific component will be tested at LDR. If no ELDRS sensitivity is shown in the initial wafer lot testing, future wafer lots of those specific components will not be tested at LDR. All future critical semiconductor component wafer lots are tested at HDR. If the components test to the same level (within 15% of the previous 99/90 RLAT level) or better as the wafer lot used to characterize the converter family, the converter family is not re-characterized. If the components test to a worse level, one of the following actions is performed (depending on the test level passed):

1. Component lot is not used in VPT RHA assured product.
2. Component lot is used if WCA (Worst Case Analysis) performed on the new lot against the original characterization WCA determines the component level characterized will not negatively impact the assured product characterization level.
3. The assured product is re-characterized using the new component lot.

EFFICIENCY PERFORMANCE CURVES ($T_{CASE} = 25^{\circ}C$, Full Load, Unless Otherwise Specified)

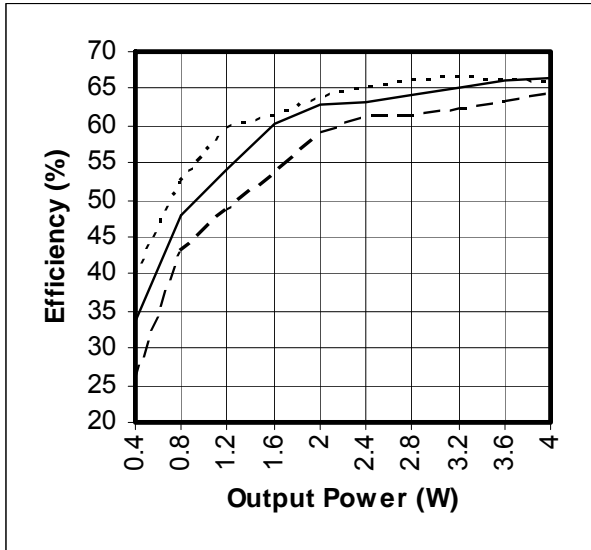


Figure 8 – SVSA283R3S
Efficiency (%) vs. Output Power (W)

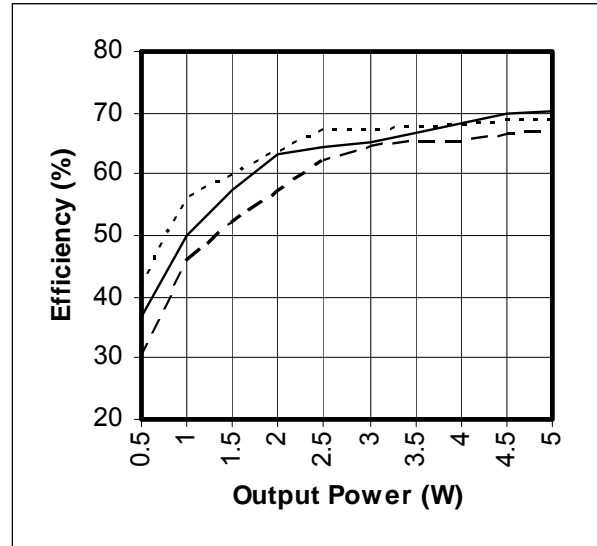


Figure 9 – SVSA2805S / SVSA285R2S
Efficiency (%) vs. Output Power (W)

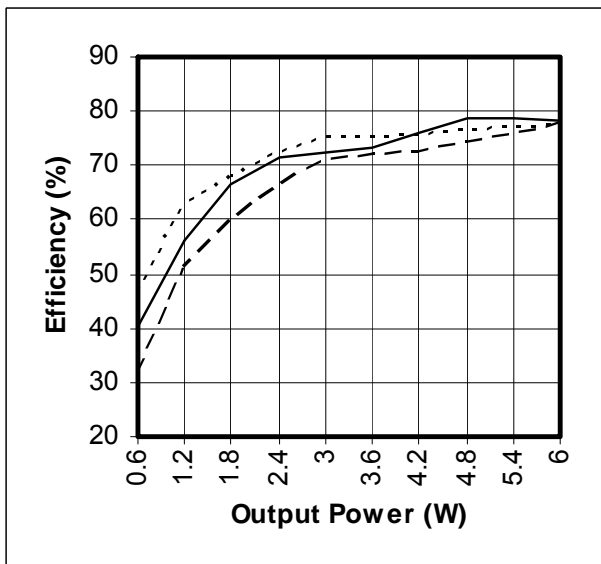


Figure 10 – SVSA2812S
Efficiency (%) vs. Output Power (W)

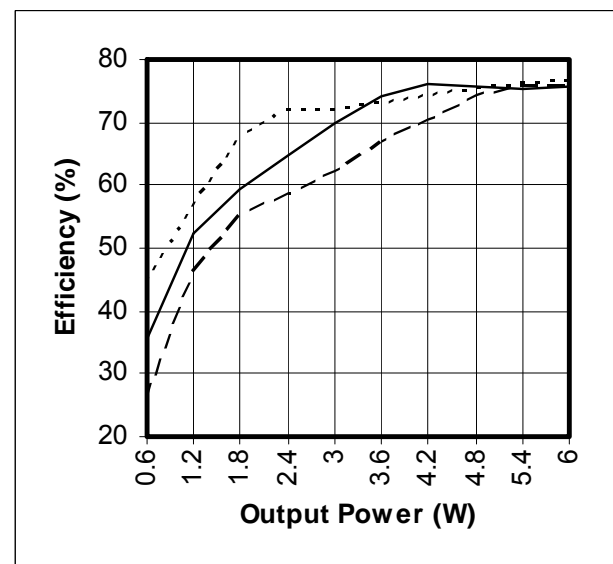


Figure 11 – SVSA2815S
Efficiency (%) vs. Output Power (W)

EMI PERFORMANCE CURVES

($T_{CASE} = 25^{\circ}C$, $V_{IN} = +28V \pm 5\%$, Full Load, Unless Otherwise Specified)

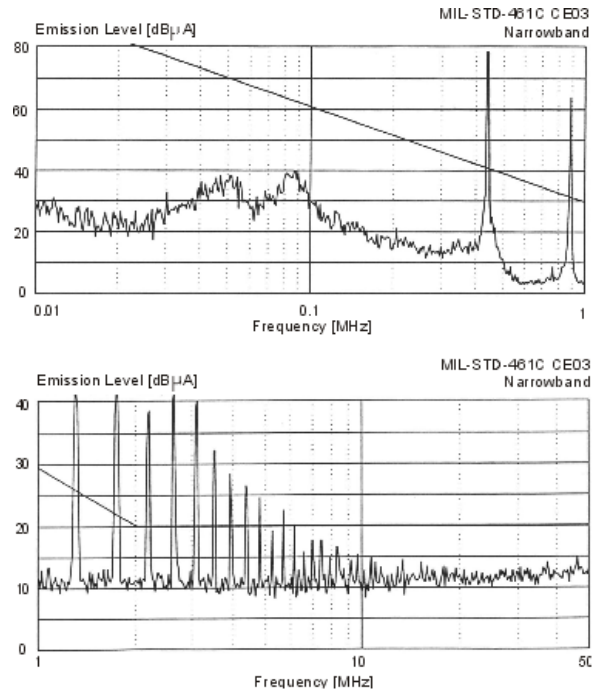


Figure 12 – SVSA2800S without EMI Filter

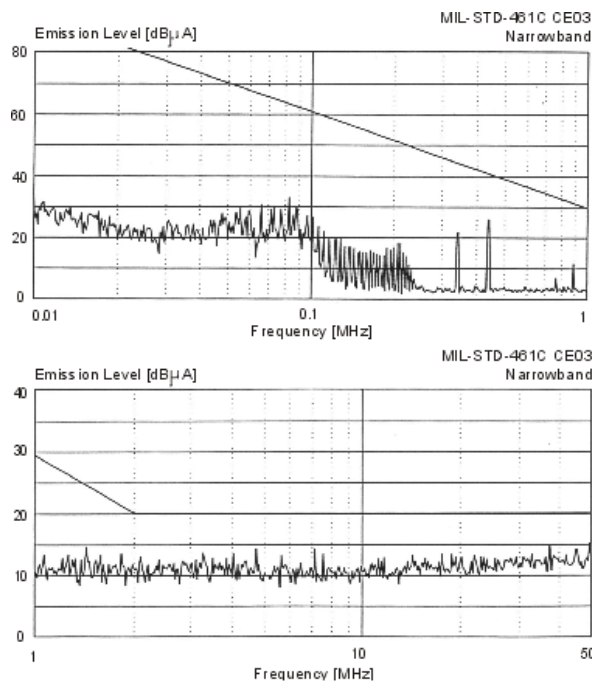
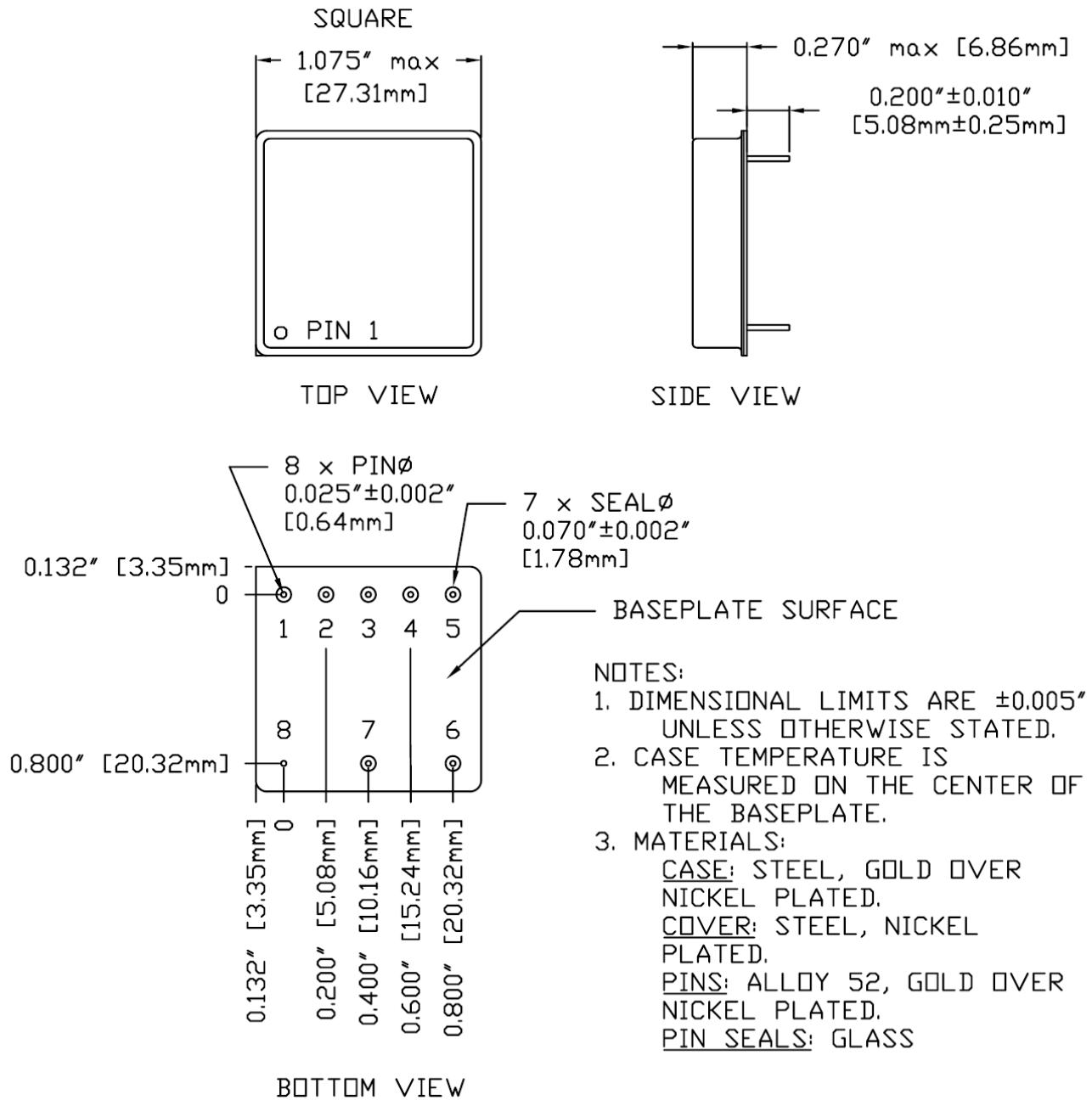


Figure 13 – SVSA2800S with EMI Filter

PACKAGE SPECIFICATIONS



PIN	FUNCTION	PIN	FUNCTION
1	+V OUT	5	INHIBIT
2	OUT COM	6	28V IN
3	N/C	7	IN COM
4	N/C	8	CASE

Figure 14 – Package and Pinout

PACKAGE PIN DESCRIPTION

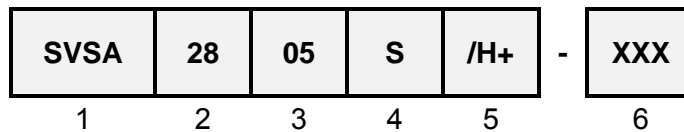
Pin	Function	Description
1	+V OUT	Positive Output Voltage Connection
2	OUT COM	Output Common Connection
3	N/C	No Connection
4	N/C	No Connection
5	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.
6	28V IN	Positive Input Voltage Connection
7	IN COM	Input Common Connection
8	CASE	Case Connection

ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Screening	MIL-STD-883	Class H+ /H+	Engineering Model ⁵ /EM
Non-Destructive Bond Pull	Method 2023	•	•
Internal Visual	Method 2017, 2032 Internal Procedure	•	•
Temperature Cycling	Method 1010, Condition C	•	
Constant Acceleration	Method 2001, 3000g, Y1 Direction	•	
PIND	Method 2020, Condition A ²	•	
Burn-In	Method 1015, 160 hours at +125°C 24 Hours at +125°C	•	•
Final Electrical	MIL-PRF-38534, Group A ¹ 100% at 25°C	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 ⁻³)	• •	•
External Visual	Method 2009	•	•

- Notes:
- 100% R&R testing at -55°C, +25°C, and +125°C with all test data included in product shipment.
 - PIND test Certificate of Compliance included in product shipment. This is an additional screening test not required per MIL-PRF-38534, Class H.
 - Non-Destructive bond pull per Method 2023 performed. This is an additional screening test not required per MIL-PRF-38534, Class H.
 - Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing options desired.
 - Engineering models utilize only the standard screening specified and are not considered compliant for flight use.

ORDERING INFORMATION



(1) Product Series	(2) Nominal Input Voltage		(3) Output Voltage		(4) Number of Outputs	
SVSA	28	28 Volts	3R3 05 5R2 12 15	3.3 Volts 5 Volts 5.2 Volts 12 Volts 15 Volts	S	Single

(5) Screening Code	(6) Additional Screening Code
/H+ /EM	Class H+ Engineering Model Contact Sales

Note: Engineering models utilize only the standard screening specified and are not considered compliant for flight use. These models are intended for low volume engineering characterization. The customer must place the following statement on each line item of their purchase order(s) for /EM units when ordering engineering models:

“(Customer Name) acknowledges that the /EM unit listed in this line item is not permitted for flight use and will be used for Engineering characterization only.”

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	SVSA2800S Series Similar Part Number
*T.B.D.	SVSA283R3S/H+
*T.B.D.	SVSA2805S/H+
*T.B.D.	SVSA285R2S/H+
*T.B.D.	SVSA2812S/H+
*T.B.D.	SVSA2815S/H+

VPT's certified radiation program per MIL-PRF-38534, Appendix G is currently under review by DSCC. Please contact DSCC at 614-692-0585 for details. Standard MIL-PRF-38534 Class H SMD's are available. Please see the standard product DVSA2800S datasheet for details.

Do not use the SVSA2800S Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DSCC website at <http://www.dscclia.mil/programs/smcr/>. The SMD number listed above is for MIL-PRF-38534 Class H screening + PIND Testing, standard gold plated lead finish, and "P" RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.

CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 353-3010
Fax: (425) 353-4030
E-mail: vptsales@vpt-inc.com

All information contained in this datasheet is believed to be accurate, however, no responsibility is assumed for possible errors or omissions. The products or specifications contained herein are subject to change without notice.